TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC11F,TC74VHC11FN,TC74VHC11FT

Triple 3-Input AND Gate

The TC74VHC11 is an advanced high speed CMOS 3-INPUT AND GATE fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

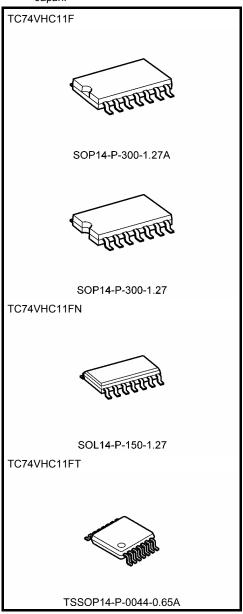
The internal circuit is composed of 4 stages including buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5~V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5~V to 3~V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: $t_{pd} = 4.1 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- · Power down protection is provided on all inputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 5.5 V
- Pin and function compatible with 74ALS11

Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

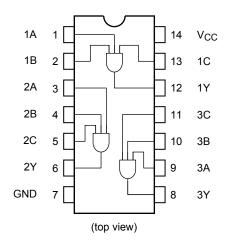
 SOP14-P-300-1.27A
 : 0.18 g (typ.)

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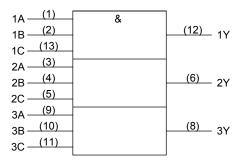
 SOL14-P-150-1.27
 : 0.12 g (typ.)

 TSSOP14-P-0044-0.65A
 : 0.06 g (typ.)

Pin Assignment



IEC Logic Symbol



Truth Table

Α	В	С	Υ	
L	Χ	Х	L	
Х	L	Х	L	
Х	Х	L	L	
Н	Н	Н	Н	

X: Don't care

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	٧
Input diode current	lıK	-20	mA
Output diode current	I _{OK}	±20	mA
DC output current	I _{OUT}	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

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Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V_{CC}	2.0 to 5.5	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
input rise and rail tille	uuuv	0 to 20 (V _{CC} = 5 ± 0.5 V)	113/V	

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	- Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
, ,				V _{CC} (V)	Min	Тур.	Max	Min	Max	
High-level input		_		2.0	1.50	_	_	1.50	_	
voltage	V _{IH}			3.0 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	V
Low-level input				2.0		_	0.50	_	0.50	
voltage	V_{IL}		_	3.0 to 5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V
	Voн	$V_{IN} = V_{IH}$		2.0	1.9	2.0	_	1.9	_	
			$I_{OH} = -50 \mu A$	3.0	2.9	3.0	_	2.9	_	
High-level output voltage				4.5	4.4	4.5	_	4.4	_	V
			$I_{OH} = -4 \text{ mA}$	3.0	2.58		_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94		_	3.80		
	V _{OL}	V _{IN} = V _{IH} or V _{IL}		2.0		0.0	0.1	_	0.1	
			$I_{OL} = 50 \mu A$	3.0	_	0.0	0.1	_	0.1	
Low-level output voltage				4.5	_	0.0	0.1	_	0.1	V
			I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44	
			I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ

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AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

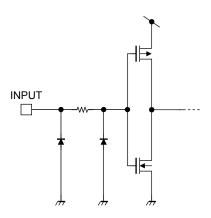
Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	
Propagation delay time	^t pLH ^t pHL	_	3.3 ± 0.3	15	_	6.1	8.8	1.0	10.5	ns ns
				50	_	8.6	12.3	1.0	14.0	
			5.0 ± 0.5	15	_	4.1	5.9	1.0	7.0	
				50	_	5.6	7.9	1.0	9.0	
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)	_	17	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

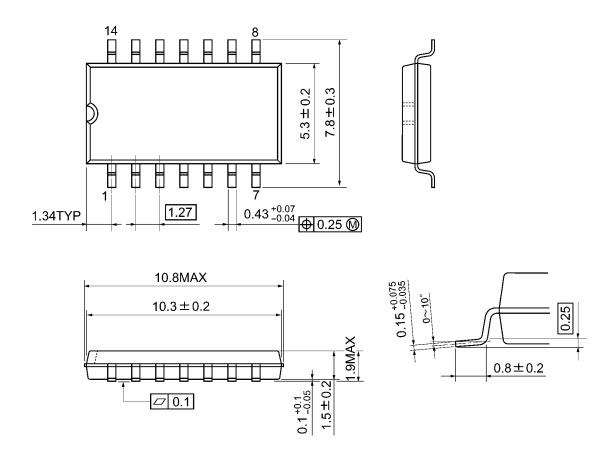
$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3 \text{ (per gate)}$$

Input Equivalent Circuit



Package Dimensions

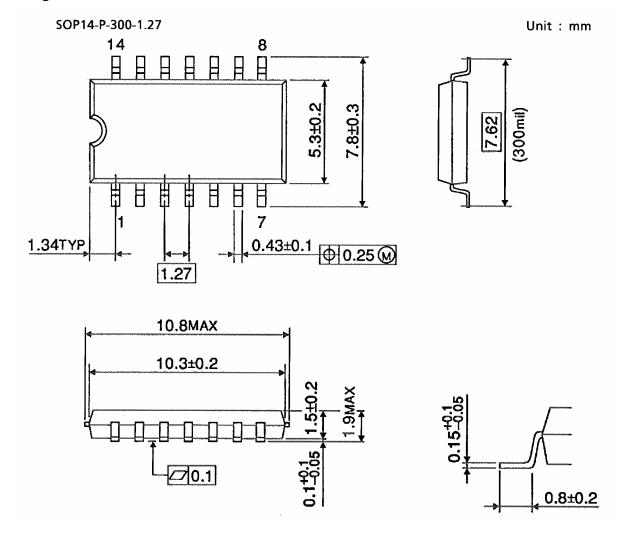
SOP14-P-300-1.27A Unit: mm



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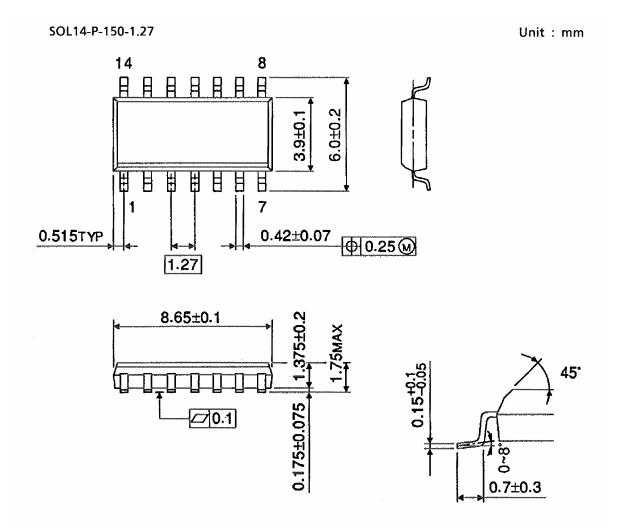
Weight: 0.18 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Package Dimensions

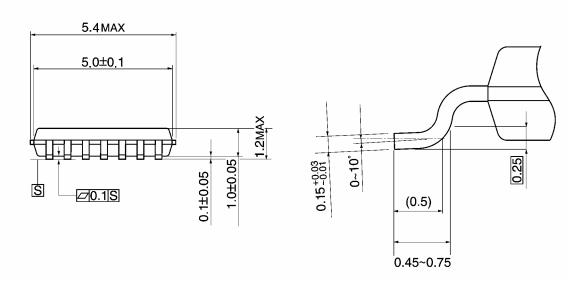
TSSOP14-P-0044-0.65A

Unit: mm

0.55TYP

0.55TYP

0.55TYP



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Weight: 0.06 g (typ.)

Note: Lead (Pb)-Free Packages

SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A

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